

Figure 2.5

DMC-1500A1000 Architecture



Figure 3.3

Data Memory

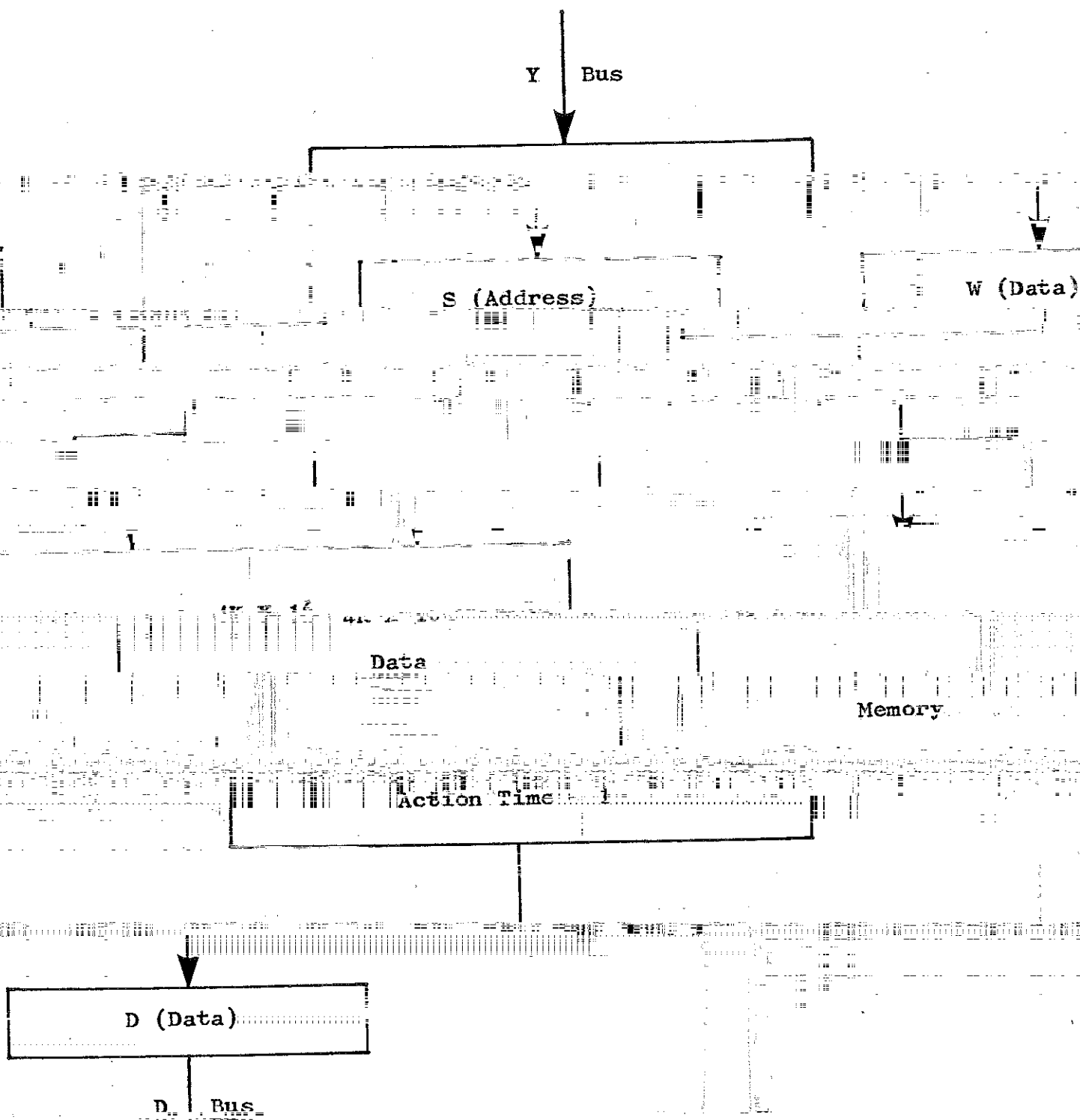
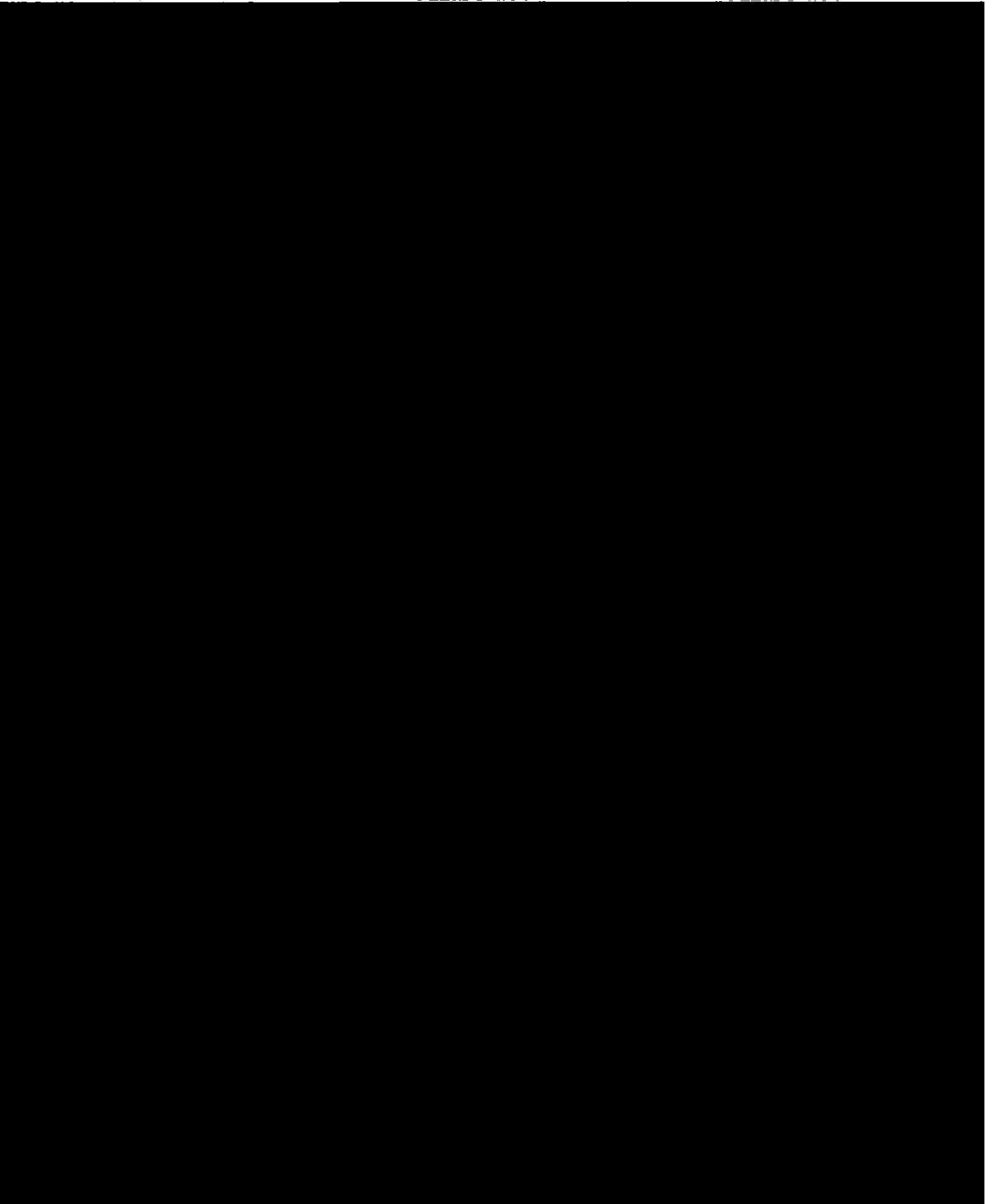


Figure 3.4





QAD

21 20

Figure

5

Figure

QAD

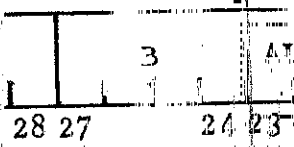
10

CN	CND	INSEL
7	6	3 2 0

EX-1000

AOR

X



ADD

performs two's complement addition of the two operands

if the result is negative, the sign bit, i.e., if arithmetic overflow occurred during the operation, the result is cleared otherwise

C set if the MSB overflowed; clear otherwise

Variations:

Mnemonic	Operation	Microcode Field Values	
		AOP	CN
ADDAQ	$A + Q$	00	0
ADDAB	$A + B$	01	0
ADDDA	$D + A$	05	0
ADDQD	$D + Q$	06	0

