

The School of Engineering Science invites applications for the following position(s):



**E**NSC 350 Digital Systems Design

1

- Knowledge and experience using VHDL to design circuits
- Experience designing circuits that interface with a bus
- Previous experience synthesizing FPGA Designs
- Experience with the Altera tool flow
- Written and one-to-one oral communication skills
- Time management skills

The University is committed to the principle of equity in employment

